Taxonomy of Power Converter Control Schemes based on the Complex Frequency Concept

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Abstract—This paper proposes a taxonomy of power converter control schemes based on the recently proposed concept of complex frequency. This quantity captures local frequency variations due to the change of both the phase angle and amplitude of bus voltages and current injections. The paper derives the analytical expressions of the link between complex power variations and complex frequency of each converter controller as well as the identification of critical control parameters. The main contribution of this work is to provide a general framework that allows classifying converters synchronization mechanisms and controllers. This framework also allows comparing converters with synchronous machines. To validate the theoretical results, extensive simulations are performed using a modified version of the WSCC 9-bus system. Examples of how the theoretical formulations of the paper can be used to improve power converter control in power system applications are showcased.

Index Terms—Complex frequency, grid-following, grid-forming, power converter, synchronization, frequency control.

I. INTRODUCTION

A. Motivations

The high penetration of converter-interfaced energy resources in modern power grids makes necessary the contribution of these devices to the frequency regulation of the network [1]. A variety of such converter-based, frequency regulating strategies has been proposed in the literature [2, 3]. The stability and transient operation of these controllers is well studied and documented [2, 4]. However, the contribution of each controller to the frequency at the converter connection point has not been yet fully discussed. In this paper, the recently proposed concept of Complex Frequency (CF) is employed to fill precisely this gap and discuss, through a rigorous analytical appraisal, the effect of different power converter controllers on the frequency at the converter connection bus. The results are organized in a systematic taxonomy of power converter synchronization and control schemes.

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B. Literature Review

Converter control schemes are commonly grouped into the Grid-Following (GFL) or Grid-Forming (GFM) categories [5, 6]. The former are meant to measure or estimate the frequency of the grid while passively injecting the power that is requested from them. The synchronization is usually achieved by means of a Phase-Locked Loop (PLL) device [4,6]. Extensive research regarding PLL stability and synchronization capability, especially in weak grids, has been carried out [7, 8]. However, GFL converters with PLLs are commonly assumed to not affect the frequency at their connection point. GFM converters do not rely on measuring or estimating the grid voltage. Instead, they achieve synchronization with the grid by varying the active power injection [4]. Various power-based synchronization strategies can be found in the literature [2-4]. Although the optimal GFM control method is still an open research field, the most common ones found both in the literature and in practical applications are the droop control and the Virtual Synchronous Machine (VSM) [9-13]. Droop controlled converters adjust their active power output through an active power-frequency (P/f) droop rule to achieve synchronization [9, 10]. VSM-based converters fully emulate a swing equation within their control structure, typically of second order [11-13]. Many VSM implementations exist with all recent works highlighting the necessity of inner current and voltage controllers in a cascaded configuration [14]. These cascaded loops allow the explicit inclusion of voltage and current limitations that are necessary for the safe operation of the converters [15]. Moreover, dedicated voltage control loops are required for the operation of the converter as a voltage source [4]. It is thus of interest to quantify the effect of all these internal (current, voltage, PLL) and external (droops, VSM) controllers on the frequency at the converter ac bus.

The precise definition of the frequency of a power system is an open research topic which has recently received renewed attention [16]. In [17], the point was raised that frequency is not uniform in the whole network, especially in transient conditions, and a formula to estimate the frequency at each system bus is proposed. In [18], the concept of CF is proposed as an extension of the well-known definition of frequency as the time derivative of the argument of a sinusoidal signal [19]. This complex variable quantifies the change of the network frequency caused by the variation of both the phase angle and the magnitude of the voltage. The interpretation of CF is approached in the literature from various viewpoints [20]. The imaginary part of the frequency is, in effect, the conventional quantity that is commonly utilized, in signal processing and time-frequency analysis, to define the *instantaneous frequency* of a signal. Using the same signal processing approach, the real part of the frequency can be defined as *instantaneous bandwidth* [21]. The geometric approach, presented in [22], assumes that the voltage (current) is *the speed of a trajectory in space*. This is supported by the fact that the voltage (current) is the time derivative of a time-varying flux (electric charge). This approach leads to define the real and imaginary parts of the CF as the symmetrical and anti-symmetrical components, respectively, of the time derivative of the voltage (current). Finally, in [23], based again on the analogy between voltage (current) and the speed of a trajectory, the real part of the CF is interpreted as a *radial speed*, whereas the imaginary part is interpreted as an *azimuthal speed*.

CF has been utilized to develop novel approaches in power system state estimation [24] as well as to study the synchronization stability of converters using dispatchable virtual oscillator control [25, 26]. However, the various converter control schemes that are found in the literature have not been studied, thus far, under the lens of CF. This paper aims at filling this gap. In [27], the magnitude of the complex frequency was utilized as a metric to compare the performance of converter primary frequency and voltage controllers. In the same spirit, the present work is an application of the concept of complex frequency to the characterization of the dynamic behavior of the different parts that form the synchronization of the control of power electronics converters.

C. Contributions

The contributions of this paper are summarized as follows:

- The notion of voltage CF presented in [18] is extended to other variables and signals. It is explained how CF can be used as derivative operator for these signals, e.g., currents or voltage references.
- Then, the generalised CF quantity, i.e. resulting from the extension above, is used to derive the effect of different converter control schemes on the grid frequency. This novel approach allows the decoupling of the effect of different participating controllers and the identification of critical control parameters. These parameters can then be tuned appropriately to maximize or minimize the controller effect on the grid frequency.
- The local frequency as perceived by the converter is calculated and categorized. This *internal* frequency differs from the bus frequency due to the action of the converter controllers and synchronization mechanism. A parallel with the rotor speed of a synchronous machine is drawn, allowing the study of the two generation types (synchronous and asynchronous) using the same theoretical tools. Examples of how the internal frequency can be used to improve converter control are included.

The theoretical contributions of this work are supported by extended simulations taking into account various controller configurations, both GFL and GFM, and the operation of a realistic grid benchmark.

D. Paper Organization

The organization of the rest of the paper is as follows. In Section II, the theoretical framework for the use of CF as a derivative operator is established. In Section III, some general mathematical derivations that establish the relationship between frequency and complex power are presented. Then, they are used to define some common special cases in power systems as well as to analyze the fundamental current controller of power converters. Sections IV and V present the effect on CF of GFL and GFM control schemes, respectively. Section VI validates the theoretical results with a case study based on the modified WSCC 9-bus system. Lastly, Section VII draws conclusions and outlines future work.

II. COMPLEX FREQUENCY AS A DERIVATIVE OPERATOR

As it is well known, a complex quantity \overline{u} can be written either in polar or rectangular coordinates, as follows:

$$\overline{u} = u_d + \jmath \, u_q = u \exp(\jmath \varphi) \,, \tag{1}$$

where $u = \sqrt{u_d^2 + u_q^2}$ and $\varphi = \arctan(u_q/u_d)$. For $u \neq 0$, the polar form can be also written as:

$$\overline{u} = \exp\left(\ln(u) + \jmath\varphi\right). \tag{2}$$

Throughout this paper, the annotation from (1) will be used to signify all complex quantities referred to a dq-axis reference frame, including currents, voltages, control signals etc. The time derivative of (2) gives:

$$\dot{\overline{u}} = (\dot{u}/u + \jmath\dot{\varphi}) \ \overline{u} = (\rho_u + \jmath\omega_u) \ \overline{u} = \overline{\eta}_u \ \overline{u} \,. \tag{3}$$

The quantity $\overline{\eta}_u$ has been utilized in [18] to define the concept of CF of time-dependent Park vectors of the voltage and of the injected current at a bus h, as follows:

$$\overline{v}_h = \overline{\eta}_v \, \overline{v}_h \,, \qquad \overline{\imath}_h = \overline{\eta}_i \, \overline{\imath}_h \,. \tag{4}$$

The CF includes a real part, which represents a *translation* and depends only on the magnitude of the Park vector; and an imaginary part, which represents a *rotation* and depends only on the phase angle of the Park vector. These two quantities can be viewed as a special case of the symmetric and anti-symmetric components of the more general concept of geometric frequency defined in [22]. The advantage of defining the real part of the CF is twofold: (i) it provides a quantity that has the same transient nature and same units as the conventional instantaneous frequency, and is thus directly comparable to it; and (ii) it allows a consistent description and formulation of the dynamic effect of the devices that are connected to the grid.

Note that, in general and in transient conditions, $\overline{\eta}_v \neq \overline{\eta}_i$, as discussed in [18]. Moreover, note that quantities $\overline{\eta}_v$ and $\overline{\eta}_i$ represent the variations of voltage and current frequency, respectively, from the nominal, steady-state values $\overline{\eta}_{vn} = \overline{\eta}_{in} = 0 + j\omega_n$, where $\omega_n = 2\pi f_n$ is the nominal frequency of the grid.

The rotation of the dq-axis coordinates has an important effect on the CF if this rotation is time dependent. Relevantly, this is the case of synchronous machines [18], and converters

that are synchronized to the grid by means of a synchronization control strategy (active power synchronization for GFM converters [9, 15], or PLL for GFL converters [4, 6]). The rotation from the local to the grid reference frame can be written as:

$$\overline{v}_h = \exp(j\delta) \,\overline{v}'_h \,, \qquad \overline{\imath}_h = \exp(j\delta) \,\overline{\imath}'_h \,, \tag{5}$$

where ' denotes variables in the local (i.e., device-side) reference frame and δ is the angle variation between the two reference frames. For example, in the case of the synchronous machine, δ refers to the transformation from the rotor reference frame to the stator while for the case of converters, it transforms quantities from the converter reference frame to the network reference frame using the angle of the controller. Note that the rotation (5) is power invariant as it does not vary voltage and current magnitudes.

Differentiating the expression of \overline{v}_h in (5) gives:

$$\begin{aligned} \dot{\overline{v}}_{h}^{\prime} &= -j \, \dot{\delta} \exp(-j \, \delta) \, \overline{v}_{h} + \exp(-j \, \delta) \, \overline{\eta}_{v} \, \overline{v}_{h} \,, \\ &= (\overline{\eta}_{v} - j \, \dot{\delta}) \, \overline{v}_{h}^{\prime} \,. \end{aligned} \tag{6}$$

Hence, the CF of a voltage referred to a local rotating reference frame is:

$$\overline{\eta}_v' = \overline{\eta}_v - \jmath \dot{\delta} \,. \tag{7}$$

A similar expression can be obtained for the current:

$$\overline{\eta}_i' = \overline{\eta}_i - \jmath \dot{\delta} \,. \tag{8}$$

In the remainder of the paper, the operators $\overline{\eta}$ and $\overline{\eta}'$ are utilized to indicate the time derivative of relevant timedependent complex quantities (e.g., the state variables and reference currents of the converter controllers) on grid and local reference frames, respectively.

III. GENERAL DERIVATIONS AND CURRENT CONTROL

Using Park vectors, the instantaneous power injected at a bus h can be written as a complex quantity, as follows:

$$\overline{s}_h = \overline{v}_h \,\overline{\imath}_h^* \,, \tag{9}$$

where * denotes the complex conjugate operator. The time derivative (rate of change) of complex power is:

$$\begin{aligned} \dot{\overline{s}}_{h} &= \dot{\overline{v}}_{h} \, \overline{\imath}_{h}^{*} + \overline{v}_{h} \, \dot{\overline{\imath}}_{h}^{*} \\ &= \overline{\eta}_{v} \, \overline{v}_{h} \, \overline{\imath}_{h}^{*} + \overline{v}_{h} \, \overline{\eta}_{i}^{*} \, \overline{\imath}_{h}^{*} \\ &= (\overline{\eta}_{v} + \overline{\eta}_{i}^{*}) \, \overline{s}_{h} \,. \end{aligned} \tag{10}$$

Using (7) and (8), (10) can be rewritten using the complex frequencies on the device local reference frame:

$$\dot{\bar{s}}_{h} = \left[\overline{\eta}_{v} + j \dot{\delta} - j \dot{\delta} + \overline{\eta}_{i}^{*} \right] \overline{s}_{h}
= \left[\overline{\eta}_{v}' + (\overline{\eta}_{i}')^{*} \right] \overline{s}_{h} .$$
(11)

Expression (10) is general for any device connected to the grid while expression (11) is general for any device that has a synchronization mechanism that aligns the dq reference frame of the device with that of the grid.

A. Ideal controllers

The most common ideal models of devices connected to the grid are considered in this section as they represent the operation of ideal (i.e., perfect tracking and infinitely fast) generators and converter controllers. In the continuation, (10) is coupled with the characteristics of each device. This allows to determine the impact of these ideal devices on the CF of the voltage and the current.

1) Ideal Voltage Source (Slack Bus): An ideal slack bus has:

$$\overline{v}_h = 0, \quad \overline{\eta}_v = 0, \tag{12}$$

From (12), (10) becomes:

$$\dot{\overline{s}}_h = \overline{\eta}_i^* \, \overline{s}_h \,, \tag{13}$$

which indicates that an ideal slack bus affects only the frequency of the injected current, but not the voltage.

2) *Ideal Current Source:* An ideal current source that can impose both the magnitude and the phase of the current:

$$\bar{i}_h = 0, \quad \bar{\eta}_i = 0,$$
(14)

leading to: $\frac{1}{\bar{s}_{b}} = \bar{n}_{c} \bar{s}_{b}$

$$\dot{\overline{s}}_h = \overline{\eta}_v \, \overline{s}_h \,. \tag{15}$$

3) Current Source with Constant Power Factor: More commonly, an ideal current source imposes the magnitude and a constant power factor [18]. This results in:

$$\rho_i = 0, \quad \omega_v = \omega_i \,, \tag{16}$$

and thus:

$$\overline{s}_h = \rho_v \overline{s}_h \,. \tag{17}$$

4) Constant Power: For and ideal constant power load or source:

$$\overline{s}_h = 0, \quad \overline{\eta}_v = -\overline{\eta}_i^*, \tag{18}$$

which indicates that the complex frequencies of both voltage and current change, but not independently.

5) *Constant Admittance:* For an ideal admittance, the magnitude of current is proportional to the one of the voltage while their phases vary only by the constant angle of the admittance. This directly dictates that [18]:

$$\rho_v = \rho_i \,, \quad \omega_v = \omega_i \,, \tag{19}$$

and from (10):

$$\dot{\bar{s}}_h = 2\rho_v \bar{s}_h \,. \tag{20}$$

This is consistent with the fact that an admittance cannot affect the frequency at a bus. Equations (17) and (20) reveal that an ideal admittance and an ideal current source with constant power factor have a similar dynamic response.

6) *PV Generator:* Lastly, an ideal PV generator has constant voltage magnitude and constant active power:

$$\rho_v = 0, \quad \dot{p} = 0.$$
(21)

Combining the two and substituting in (10) leads to:

$$\frac{q}{p} = \frac{\rho_i}{(\omega_v - \omega_i)} \,. \tag{22}$$

where p and q are the active and reactive power components of \overline{s}_h , respectively. Equation (22) leads to the non-intuitive conclusion that the reactive power at a PV bus can be changed



Fig. 1. Control diagram of a PI-based current control in the synchronous reference frame. Optional VFF are specifically marked with dotted boxes.

not only by varying the voltage magnitude but also by varying either the frequency of the voltage and/or of the current.

B. Current Controller

Due to practical considerations (e.g., over-current limiting and resonance damping) an inner current controller is ubiquitous in VSC applications [4]. Fig. 1 shows a control diagram of such a controller. The dynamic equation of a current flowing through the output inductor L of a converter is:

$$\overline{v}_t - \overline{v}_h = L_f \, \dot{\overline{i}}_h + R_f \, \overline{i}_h, \tag{23}$$

where L_f is the filter inductance, R_f its parasitic resistance and $\overline{v}_t = v_{d,t} + jv_{q,t}$ is the modulated voltage output of the converter. From Figure 1, the control equations can be derived as:

$$\overline{v}_t = \overline{v}'_h + \jmath \omega_n L_f \,\overline{i}'_h + K_p (\overline{i}_{ref} - \overline{i}'_h) + K_i \,\overline{x} \,,
\dot{\overline{x}} = \overline{i}_{ref} - \overline{i}'_h$$
(24)

where K_p , K_i are the proportional and integral gains of the PI controller, respectively, $\overline{x} = x_d + \jmath x_q$ is the complex internal state of the integral part of the controller and $\overline{i}_{ref} = i_{d,ref} + \jmath i_{q,ref}$ is the current reference signal. By neglecting the synchronization mismatches $(\overline{v}'_h \approx \overline{v}_h, \overline{i}'_h \approx \overline{i}_h)$, the electromagnetic dynamics of the *L*-filter $(L_f \overline{i}_h = L_f \overline{\eta}, \overline{i}_h \approx \omega_n L_f \overline{i}_h)$ and its resistance $(R_f \approx 0)$, and by substituting equation (24) to (23), one derives [28]:

$$0 = K_p(\bar{\imath}_{ref} - \bar{\imath}'_h) + K_i \,\bar{x} \,,$$

$$\dot{\bar{x}} = \bar{\imath}_{ref} - \bar{\imath}'_h \,.$$
 (25)

Differentiating the algebraic equation in (25), one has:

$$\overline{\eta}'_{i} \overline{i}'_{h} = \overline{i}_{\text{ref}} + \kappa_{\text{PI}} \overline{x}
= \overline{\eta}_{i_{\text{ref}}} \overline{i}_{\text{ref}} + \kappa_{\text{PI}} (\overline{i}_{\text{ref}} - \overline{i}'_{h})
= (\overline{\eta}_{i_{\text{ref}}} + \kappa_{\text{PI}}) \overline{i}_{\text{ref}} - \kappa_{\text{PI}} \overline{i}'_{h}.$$
(26)

where $\kappa_{\rm PI} = K_i/K_p$. Equation (26) can be also written as:

$$\left(\overline{\eta}_{i}' + \kappa_{\rm PI}\right)\overline{\imath}_{h}' = \left(\overline{\eta}_{\imath_{\rm ref}} + \kappa_{\rm PI}\right)\overline{\imath}_{\rm ref},\qquad(27)$$

which shows that the coefficient κ_{PI} , and hence the PI controller effect, can be interpreted as a constant real translation that affects only the magnitude. Then, substituting (26) into (11) leads to:

$$\dot{\overline{s}}_{h} = \left(\overline{\eta}_{i_{\text{ref}}}^{*} + \kappa_{\text{PI}}\right) \overline{v}_{h}^{\prime} \,\overline{i}_{\text{ref}}^{*} + \left(\overline{\eta}_{v}^{\prime} - \kappa_{\text{PI}}\right) \overline{s}_{h} \,. \tag{28}$$



Fig. 2. Control diagram of synchronous reference frame PLL.

Equation (28) separates the effect of the inner current control from the synchronization mechanism and the outer control loops. Moreover, if the dynamic of the PI can be assumed fast, $\bar{\imath}_{ref} \approx \bar{\imath}'_h$ and hence $\bar{\eta}_{\imath_{ref}} \approx \bar{\eta}'_i$. Then (28) simplifies to (11). Finally we note that the coefficients of $\bar{\imath}'$ and $\bar{\upsilon}'$ that appears in (27) and (28), respectively, can be written as:

$$(\overline{\eta}_{i}')^{*} + \kappa_{\mathrm{PI}} = \overline{\eta}_{i}^{*} + (\kappa_{\mathrm{PI}} + \jmath \delta), \overline{\eta}_{v}' - \kappa_{\mathrm{PI}} = \overline{\eta}_{v} - (\kappa_{\mathrm{PI}} + \jmath \dot{\delta}).$$

$$(29)$$

The complex quantity $\kappa_{\rm PI} + j \delta$ has the dimension of a complex frequency and embeds the effects of the synchronization mechanism and of the current control. The equation is general and can be applied to both synchronous machines and controllers with an internal current loop. For a synchronous machine $\kappa_{\rm PI} = 0$, whereas for an ideal synchronization (e.g., ideal PLL), $j \delta \approx 0$ because $\delta \approx 0$. The utilization of the CF allows thus to easily identify and separate the dynamic effects and non-ideality of the controllers of conventional and converter-interfaced devices connected to the grid.

C. Voltage Feed-Forward (VFF)

Equation (25) assumes that a VFF is included in the current controller [28]. If the VFF is not included, (25) becomes:

$$\bar{\imath}_{h}^{\prime} = \bar{\imath}_{\mathrm{ref}} + \frac{1}{K_{p}} (K_{i} \overline{x} - \overline{v}_{h}^{\prime}), \qquad (30)$$

which leads to:

$$\left(\overline{\eta}_{i}'+\kappa_{\rm PI}\right)\overline{\imath}_{h}'=\left(\overline{\eta}_{\imath_{\rm ref}}+\kappa_{\rm PI}\right)\overline{\imath}_{\rm ref}-\frac{1}{K_{p}}\overline{\eta}_{v}'\,\overline{v}_{h}'\,,\qquad(31)$$

and, finally:

$$\dot{\overline{s}}_{h} = \left(\overline{\eta}_{i_{\text{ref}}}^{*} + \kappa_{\text{PI}}\right) \overline{v}_{h}' \, \overline{i}_{\text{ref}}^{*} + \left(\overline{\eta}_{v}' - \kappa_{\text{PI}}\right) \overline{s}_{h} - \frac{1}{K_{p}} \left(\overline{\eta}_{v}'\right)^{*} v_{h}^{2} \,, \tag{32}$$

where $v_h = v'_h$ is the magnitude of \overline{v}'_h . This result indicates that the dynamic effect of the VFF is inversely proportional to K_p and that it modulates the CF of voltage \overline{v}_h .

IV. GFL CONVERTERS

GFL converters typically measure or estimate the frequency and phase of the voltage at the converter connection point and then use these measurements to synchronize with the grid [4]. Typically, this measurement is achieved through a PLL device. A common PLL synchronization strategy is to set the voltage q component equal to zero by using a PI controller. In that case, the output of the PLL provides the angle that is used for the transformation between converter-side and grid-side variables. A control diagram of a common PLL is shown in Fig. 2. The synchronization strategy is given by [29]:

$$\dot{x}_{\text{PLL}} = \Im\{\overline{v}_h'\}, \dot{\delta} = K_p^{\text{PLL}} \Im\{\overline{v}_h'\} + K_i^{\text{PLL}} x_{\text{PLL}} - \omega_v,$$
(33)



Fig. 3. Block diagram for the calculation of current reference from active and reactive power commands.

where K_p^{PLL} , K_i^{PLL} are the proportional and integral gains of the PI controller included in the PLL, \Im is the imaginary part operator and ω_v is the frequency at the connection bus. Many other PLL implementations are reported in the literature [29, 30]. Equation (33) can be substituted back into equations (7) and (8) so that the effect of the PLL parameters to the CF can be directly evaluated.

In the following subsections, expressions for the terms $\bar{\imath}_{\rm ref}$ and $\bar{\eta}_{\imath_{\rm ref}}$ (or $\bar{\eta}_{\imath_{\rm ref}}$ $\bar{\imath}_{\rm ref}$) are provided for relevant GFL control configurations that are typically found in practical applications. Then, these expressions are coupled with (28) and (33) to derive the expression of $\dot{\bar{s}}_h$.

A. Current controller with constant current reference

In this simplified case, the current controller tracks a constant current reference. Thus, the expressions for $\bar{\imath}_{ref}$ and $\bar{\eta}_{\imath_{ref}}$ are immediately deduced as:

$$\bar{\imath}_{ref} = \text{const.},$$

 $\Rightarrow \quad \bar{\eta}_{\imath_{ref}} = 0,$
(34)

(35)

and substituting into (28):

$$\dot{\overline{s}}_h = \kappa_{\rm PI} \, \overline{v}'_h \, \overline{\imath}^*_{\rm ref} + \left(\overline{\eta}'_v - \kappa_{\rm PI} \right) \overline{s}_h \, .$$

For an ideal current source, $\bar{i}_{ref} = \bar{i}$ and substituting $\bar{v}'_h \bar{i}^*_{ref} = \bar{s}_h$, (35) simplifies to (15). With the use of (25), equation (35) can be re-written in terms of the controller internal states as:

$$\dot{\bar{s}}_h = \bar{s}_h \bar{\eta}_v - \kappa_{\rm PI}^2 \bar{x}^* \bar{v}'_h = \bar{s}_h \bar{\eta}_v + \Delta \dot{\bar{s}}_h \,. \tag{36}$$

In the following, (36) is utilized to highlight the effect of the non-ideal current controller in comparison with the ideal current source of (15).

B. Current Controller with Constant Power Reference

In practical applications, a constant power reference (\bar{s}_{ref}) is often preferred over a current reference [31]. Then:

$$\bar{\imath}_{\rm ref} = \bar{\imath}_s = \left(\frac{\bar{s}_{\rm ref}}{\bar{v}'_h}\right)^* ,$$

$$\Rightarrow \quad \overline{\eta}_{\imath_{\rm ref}} \, \bar{\imath}_{\rm ref} = \left(-\frac{\bar{s}_{\rm ref}}{\bar{v}'_h} \overline{\eta}'_v\right)^*$$

$$= -\bar{\imath}_{\rm ref} \left(\overline{\eta}'_v\right)^* ,$$

$$\Rightarrow \quad \overline{\eta}_{\imath_{\rm ref}} = -(\overline{\eta}'_v)^* ,$$
(37)

leading to:

$$\dot{\overline{s}}_{h} = \left(\overline{\eta}_{v}' - \kappa_{\rm PI}\right) \left(\overline{s}_{h} - \overline{s}_{\rm ref}\right). \tag{38}$$

For an ideal PQ source $\overline{s}_h = \overline{s}_{ref}$, which leads to simplify (38) to (18). A block diagram for the calculation of the current



Fig. 4. Control diagram of a virtual admittance loop.



Fig. 5. Control diagram of the outer loops for GFL applications.

reference from the active and reactive power commands is shown in Fig. 3.

C. Current Controller with Virtual Admittance Loop

Reference [32] proposes a virtual admittance control loop able to provide voltage support in resistive or weak grids. A control diagram of this loop can be seen in Fig. 4. This loop emulates the operation of an admittance $\overline{Y}_v = G_v + jB_v$ in order to follow a voltage reference $\overline{v}_{ref} = v_n + j0$, where v_n is the nominal voltage ($\overline{v}_{ref} = \overline{v}_{ref}^*$). In the literature, converters that provide ancillary services to the grid are also referred to as grid-supporting converters [5]. However, since these controllers utilizes identical PLL and current controllers, they can be included in the GFL category for ease of reference. The equations are:

$$\bar{\imath}_{\rm ref} = \bar{\imath}_s + \overline{Y}_v (\overline{v}_{\rm ref} - \overline{v}'_h) \\
= (\frac{\overline{s}_{\rm ref}}{\overline{v}'_h})^* + \overline{Y}_v (\overline{v}_{\rm ref} - \overline{v}'_h) , \qquad (39)$$

$$\overline{\eta}_{\imath_{\rm ref}} \bar{\imath}_{\rm ref} = \left(-\frac{\overline{s}_{\rm ref}}{\overline{v}'_h} \overline{\eta}'_v \right)^* - \overline{Y}_v \overline{v}'_h \overline{\eta}'_v ,$$

where $\bar{\imath}_s$ denotes the current calculated by the power reference as in the previous case. By substituting:

 \Rightarrow

$$\overline{s}_{h} = (\overline{\eta}'_{v} - \kappa_{\mathrm{PI}}) (\overline{s}_{h} - \overline{s}_{\mathrm{ref}})
+ \overline{Y}_{v}^{*} (-v_{h}^{2} ((\overline{\eta}'_{v})^{*} + \kappa_{\mathrm{PI}}) + \kappa_{\mathrm{PI}} \overline{v}'_{h} \overline{v}_{\mathrm{ref}}).$$
(40)

Assuming ideal current control ($\kappa_{PI} = 0$), (40) simplifies to:

$$\dot{\overline{s}}_h = -v_h^2 \overline{Y}_v^* 2\rho_v + \overline{Y}_v^* \overline{\eta}_v' \overline{v}_h' \overline{v}_{\rm ref} \,. \tag{41}$$

The first term corresponds to the power consumed by the virtual admittance and is consistent with (20), whereas the second term corresponds to the voltage reference.

D. Current Controller with Outer Control Loops

GFL converters can be used for applications different than power delivery from distributed sources [31]. Most notable is the case of GFL rectifiers that are tasked with maintaining dc-link voltage v_{dc} equal to its nominal value $v_{n,dc}$ of an electronically-interfaced load [33]. This is achieved through an outer-loop that calculates the *d*-axis current reference [28, 34]. Therefore, the *q*-axis current is free to provide voltage support. This is usually achieved through an outer droop controller but a PI regulator can also be used [35, 36]. In this study, a PI is used for generality. A control diagram of these outer loops can be seen in Fig. 5.

The current references are calculated as:

$$u_{d,\text{ref}} = K_p^{\text{dc}}(v_{n,\text{dc}} - v_{\text{dc}}) + K_i^{\text{dc}} x_{\text{dc}},
\mu_{q,\text{ref}} = K_p^{\text{ac}}(v_n - v_h) + K_i^{\text{ac}} x_{\text{ac}},$$
(42)

where K_p^{dc} , K_i^{dc} are the proportional and integral gains, respectively, of the outer dc voltage PI controller, K_p^{ac} , K_i^{ac} are the proportional and integral gains, respectively, of the outer ac voltage PI controller, v_n is the nominal voltage rate of the connection bus, and the internal PI states are:

$$\begin{aligned} \dot{x}_{\rm dc} &= v_{n,\rm dc} - v_{\rm dc} \,, \\ \dot{x}_{\rm ac} &= v_n - v_h \,. \end{aligned} \tag{43}$$

By using the definition of the real part of the CF (3), equation (42) and its derivative can be written using complex notation as:

$$\bar{\imath}_{\rm ref} = K_p^o(\bar{v}_{\rm ref}^o - \bar{v}_o) + K_i^o \bar{x}_o,
\Rightarrow \quad \bar{\eta}_{\imath_{\rm ref}} \bar{\imath}_{\rm ref} = -\bar{v}_o(K_p^o \bar{\rho} + K_i^o) + K_i^o \bar{v}_{\rm ref}^o,$$
(44)

where $K_p^o = K_p^{dc} = K_p^{ac}$, $K_i^o = K_i^{dc} = K_i^{ac}$, $\overline{x}_o = x_{dc} + jx_{ac}$, $\overline{v}_{ref}^o = v_{n,dc} + jv_n$, $\overline{v}_o = v_{dc} + v_h$ and $\overline{\rho} = \rho_{dc} + j\rho_v$. The assumption of equal controller gains for the two dq-axes is consistent if a proper pu base for the dc voltage is selected so that $v_{n,dc} = v_n$. This is straightforward as the dc voltage is independent from the rest of the system and typically ac and dc voltages are selected in the same order of magnitude [28]. Equation (44) indicates that the outer control loops only affect quantity $\overline{\rho}$ (i.e., the magnitude of ac and dc voltage) but not frequency ω_v .

V. GFM CONVERTERS

In this section, the analytical expressions for $\bar{\imath}_{\rm ref}$, $\bar{\eta}_{\imath_{\rm ref}}\bar{\imath}_{\rm ref}$ and $\dot{\bar{s}}$ are derived, considering the inner voltage controllers for GFM control configurations. Then, different power-based synchronization and voltage regulation strategies that are commonly found in the literature are presented.

A. Voltage Controller

A set of equations that describes a PI-based, voltage controller is the following:

$$\begin{aligned} \dot{\overline{x}}_v &= \overline{v}_{\rm ref} - \overline{v}'_h, \\ \overline{\imath}_{\rm ref} &= K^v_p (\overline{v}_{\rm ref} - \overline{v}'_h) + K^v_i \overline{x}_v, \\ \overline{\eta}_{\imath_{\rm ref}} \overline{\imath}_{\rm ref} &= (K^v_p \overline{\eta}_{v_{\rm ref}} + K^v_i) \overline{v}_{\rm ref} - (K^v_p \overline{\eta}'_v + K^v_i) \overline{v}'_h. \end{aligned}$$
(45)

where \overline{v}_{ref} is the voltage reference from an outer loop, e.g., a voltage droop controller. The control diagram of the outer



Fig. 6. Control diagram of a PI-based voltage controller used in GFM applications.



Fig. 7. Control diagram of the active power synchronization strategies. (a) P/f droop and (b) VSM angle control.

voltage controller for GFM applications is shown in Fig. 6. To decouple the effects of the voltage and the current controllers, it is assumed that $\bar{\eta}_{i_{\rm ref}}^* + \kappa_{\rm PI} \approx \bar{\eta}_{i_{\rm ref}}^*$. Thus, by substituting the third equation of (45) into (28), one has:

$$\dot{\overline{s}}_{h} = (K_{p}^{v}\overline{\eta}_{v_{\text{ref}}}^{*} + K_{i}^{v})\overline{v}_{h}^{\prime}\overline{v}_{\text{ref}}^{*}
- (K_{p}^{v}(\overline{\eta}_{v}^{\prime})^{*} + K_{i}^{v})v_{h}^{2} + (\overline{\eta}_{v}^{\prime} - \kappa_{\text{PI}})\overline{s}_{h}.$$
(46)

The above equation indicates that gain K_p^v modulates the frequency of the voltage in a similar way as gain K_p^o modulates the quantity $\overline{\rho}$ in (44).

B. Power-Based Synchronization Strategies

This section discusses the expressions for δ based on the widely known active power-frequency droop (P/f droop) and VSM concepts. Control diagrams for these two synchronization strategies can be seen in Fig. 7. Various other implementations for the outer frequency/phase controllers have been proposed and the optimal controller selection for different scenarios is still an open research topic [3].

The equations for the P/f droop are the following [9, 10]:

$$\delta = \omega_n - \omega_v - m_p (P - p_{\text{ref}}),$$

$$T_f \dot{P} = p_h - P,$$
(47)

where m_p is the droop gain and T_f is the time constant of the (optional) low-pass filter. Lowercase p_h is the active power injection and uppercase P is the filtered active power. For the VSM, a common implementation in the literature is the following [11–13]:

$$\delta = \omega_{\rm VSM} - \omega_v ,$$

$$\dot{\omega}_{\rm VSM} = \frac{1}{J_v} \left(\frac{p_{\rm ref}}{\omega_n} - \frac{p_h}{\omega_{\rm VSM}} + D_p(\omega_n - \omega_{\rm VSM}) \right),$$
(48)

where J_v is the moment of virtual inertia coefficient while D_p is the virtual damping coefficient. It should be noted that



Fig. 8. Control diagram of the outer voltage loops for GFM applications. (a) Q/V droop and (b) VSM voltage control.

moment of inertia J is a distinct quantity than the mechanical starting time M and only coincide when pu values are being used.

C. Outer Voltage Loop

This section discusses the outer voltage loop that defines the quantity $\overline{v}_{ref} = v_{d,ref} + \jmath v_{q,ref}$. By obtaining the analytical expressions for \overline{v}_{ref} and $\dot{\overline{v}}_{ref}$, they can be substituted into (46). Typically, reactive power/voltage (Q/V) droop controllers, PI/integral-based controllers or a combination of the two are preferred [3]. The control diagrams for the voltage regulation loops for the Q/V droop strategy and the VSM voltage control strategy are shown in Fig. 8.

For the droop controller, the voltage is aligned with the *d*-axis of the voltage. That results in:

$$\overline{v}_{\text{ref}} = v_{d,\text{ref}} = v_n - m_q(Q - q_{ref}),$$

$$T_f \dot{Q} = q_h - Q,$$
(49)

where lowercase q is the reactive power injection and uppercase Q is the filtered reactive power. Differentiating one gets:

$$\overline{\eta}_{v_{\rm ref}} \overline{v}_{\rm ref} = \dot{v}_{d,\rm ref} = \frac{m_q}{T_f} (Q - q_h).$$
(50)

By observing that $\overline{v}_{ref} = v_{d,ref}$ leads directly to $\overline{\eta}_{v_{ref}}\overline{v}_{ref} = (\overline{\eta}_{v_{ref}}\overline{v}_{ref})^*$, one can simplify (50) to:

$$\overline{\eta}_{v_{\rm ref}}\overline{v}_{\rm ref} = \rho_{v_{\rm ref}}v_{d,\rm ref} = \frac{m_q}{T_f}(Q-q_h).$$
(51)

An example of a combination of integral and droop controller for the voltage regulation can be found in [13]. This strategy is paired with the VSM synchronization control of equation (48). In it, the equivalence with physical quantities is preserved by defining the virtual flux as:

$$\dot{\psi}_v = K_Q(q_{\text{ref}} - q_h + D_Q(v_n - v_h)),$$
 (52)

where K_Q is the integral gain and D_Q is the droop gain. The output of the outer voltage controller is then aligned with the q-axis of the voltage as in:

$$\overline{v}_{\text{ref}} = \jmath v_{q,\text{ref}} = \jmath \psi_v \omega_{\text{VSM}} ,
\Rightarrow \overline{\eta}_{v_{\text{ref}}} \overline{v}_{\text{ref}} = \jmath (\dot{\psi}_v \omega_{\text{VSM}} + \psi_v \dot{\omega}_{\text{VSM}}) ,$$
(53)

where $\dot{\psi}_v$ is given by (52) and $\dot{\omega}_{\rm VSM}$ is given by (48). Similar to the previous case, one observes that again $\overline{\eta}_{v_{\rm ref}} = \rho_{v_{\rm ref}}$. It can be concluded then that when either the real or the imaginary part of the outer loop reference is set to zero, the outer loop

 TABLE I

 Summary of the internal frequency expressions

Control Configuration	Internal Frequency	Equations
Current control + PLL	$\overline{\eta}_v - \jmath \dot{\delta} - \kappa_{\rm PI}$	(7), (33), (35), (38)
Effect of VFF	$-\frac{1}{K_p}(\overline{\eta}_v^*+j\dot{\delta})$	(7), (32), (33)
Virtual admittance	$-v_h^2 \overline{Y}_v^* ((\overline{\eta}_v')^* + \kappa_{\rm PI})$	(7), (33), (40)
GFL outer loops	$K_p^o \overline{\rho} + K_i^o$	(44)
GFM voltage control	$-(K_p^v(\overline{\eta}_v')^* + K_i^v)$	(46)
P/f droop control	$\overline{\eta}_v - \jmath \dot{\delta}$	(7), (47)
VSM angle control	$\overline{\eta}_v - \jmath \dot{\delta}$	(7), (48)

does not modify the frequency of the reference signal, only its magnitude. This is consistent with the original design of the outer voltage loops.

For ease of reference, all the internal frequencies derived in Sections IV and V are summarized in Table I. Once these internal frequencies are defined, one can compare the dynamic performance of converter control based on the bus frequency and on the *internal* ones, and also understand how the parameters of the these controllers affect the internal frequency and, then, again, the control itself. This study is particularly relevant in the case of electronic converters as their controllers can be fast and thus even relatively small differences in the value of the measured frequency can lead to visible differences in their dynamic performance.



Fig. 9. Single-line diagram of the modified WSCC 9-bus system.

VI. SIMULATION RESULTS

To illustrate the theoretical developments, different case studies considering the WSCC 9-bus system are presented in this section. The purpose of the case study is to show how the CF can be used as a *metric* for the dynamic performance of the synchronization mechanisms and controllers of grid-connected converters. The synchronous generators are modeled using conventional 4th order models. A converter-interfaced energy storage system is connected to bus 5 of the grid through an LC-filter, modeled as in [37]. Although its dynamic effects has been neglected in the analytical developments above, the ac filter is included in the models utilized in the simulations. A schematic of the network that was used for all simulations is shown in Figure 9. The CF components ρ_v and ω_v are calculated using the procedure described in [18]. All simulations were carried out with the Dome software tool [38]. For the examples regarding the effect of the PLL and the current control, a standard SRF-PLL described by (33) is used. The current references are kept constant and tracked by a PI controller as in (25). The dc voltage is kept constant



Fig. 10. Frequency deviation referred to the converter reference frame and as seen at bus 5 of the WSCC 9-bus system during the disconnection at t = 1 s of the load at bus 5. PLL gains are set for various controller bandwidth values.



Fig. 11. Frequency deviation referred to the synchronous machine rotor and as seen at bus 1 of the WSCC 9-bus system during the disconnection at t = 1 s of the load at bus 5. The simulation is repeated for different values of M.

by an ideal voltage source. For the example on the outer controllers, the ideal voltage source is swapped for a capacitor of $C_{\rm dc} = 0.05$ F and the outer loops of (42) and (43) are used. Lastly, for the GFM configurations, the outer voltage loop of (45) are used to regulate the voltage. Equations (47) and (49) are used to define the droop-based, GFM control while (48), (52) and (53) are used for the VSM control. In the latter case, an ideal voltage source is again used to regulate the dc voltage. The contingency that is used for all examples is a disconnection of the load at bus 5 at t = 1 s. If not explicitly stated otherwise, all frequency-like quantities in this section refer to frequency deviations from the nominal values ($\rho_v = 0$ and $\omega_v = 1$ pu).

A. Effect of the PLL

Fig. 10 shows the imaginary part of the CF ω'_v referred to the converter reference frame (equation (7)) as well as the frequency ω_v , as seen at the converter bus. The PLL gains are tuned so that the PLL damping is always one while its bandwidth varied. While the frequency at the bus is unaffected by the PLL gains, the frequency referred to the converter is dictated by the PLL dynamics. A parallel for PLL case can be drawn with the case of the synchronous machine. For that device, "internal" frequency ω_v^\prime can be calculated with equation (7) where in that case, $\dot{\delta}$ is the rotor speed referred to the frequency at the center of inertia, namely $\delta = \omega_r - \omega_{COI}$. Fig. 11 shows the frequency deviation referred to the synchronous machine rotor and at bus 1 after the contingency for different values of the mechanical starting time M. The mechanical starting time is defined as M = 2 H, where H is the inertia constant of a synchronous machine [39]. The two quantities are often used interchangeably in the literature to describe the inertia properties of a synchronous machine [39, 40]. The parameter change affects both quantities, but it can be seen



Fig. 12. Real part of the CF ρ_v , translated by the effect of the PI current controller and as seen at bus 5 during the contingency for different values of $\kappa_{\rm PI}$.



Fig. 13. Real and imaginary part of the quantity $\Delta \bar{s}_h$, quantifying the nonideality of the current control. Simulation for disconnection at t = 1 s of the load at bus 5 and different values of $\kappa_{\rm PI}$.

that the internal frequency undergoes larger variations. The above examples illustrates that while the internal frequency for the synchronous machine is well defined and understood, this is not the case for the different converter controllers. The proposed method helps to overcome this problem by clearly defining it for the different synchronization methods. It is shown in Section VI-G how this method can be used for control applications.

B. Non-Ideality of the Current Controller

Fig. 12 shows the translation in the CF caused by the internal current PI as well as the real part of the CF at the converter bus. It can be seen that the variation of ρ_v is negligible compared to the constant translation caused by the PI. A better way to illustrate the non-ideality of the current controller is by plotting its deviation from an ideal current controller $\Delta \dot{s}_h$, calculated with (36). Fig. 13 shows the real and imaginary parts of this quantity for different values of parameter $\kappa_{\rm PI}$. For larger values of the parameter, the operation of the controller increasingly deviates from the ideal case, namely $\kappa_{\rm PI} = 0$.

C. Effect of the VFF

Figs. 14 and 15 show the real and imaginary part of the CF at bus 5 for different values of current control proportional gain K_p . The simulation is repeated with the VFF being switched on and off for each value of the controller gain. If the VFF is active, the change of the parameter does not affect neither of the two components of the CF. If the VFF is switched off, the gain modulates the frequency, as predicted by (32).

D. Effect of the Outer Loops

Fig. 16 shows the effect on the CF of the variation of gain K_p^o of the outer control loops in the GFL control of (44).



Fig. 14. Real part of the CF as seen at bus 5 of the WSCC 9-bus system during the disconnection at t = 1 s of the load at bus 5. Simulation for different values of parameter K_p and VFF enabled and disabled.



Fig. 15. Imaginary part of the CF as seen at bus 5 of the WSCC 9-bus system during the disconnection at t = 1 s of the load at bus 5. Simulation for different values of parameter K_p and VFF on and off.



Fig. 16. Real and imaginary part of the CF as seen at bus 5 of the WSCC 9bus system during the disconnection at t = 1 s of the load at bus 5. Simulation for different values of parameter K_p^o of the outer voltage loops in the GFL control configuration.



Fig. 17. dc "frequency" during the contingency at t = 1 s. Simulation for different values of parameter K_p^o of the outer voltage loops in the GFL control configuration.



Fig. 18. Frequency deviation referred to the converter reference frame and as seen at bus 5 of the WSCC 9-bus system during the disconnection at t = 1 s of the load at bus 5. The simulation is repeated for different values of the active power droop parameter m_p .



Fig. 19. Frequency deviation referred to the converter reference frame and as seen at bus 5 of the WSCC 9-bus system during the disconnection at t = 1 s of the load at bus 6. The simulation is repeated for different values of the VSM damping parameter D_p .

Results confirm that only ρ_v is affected while ω_v remains identical. Fig. 17 shows the variation of $\rho_{\rm dc}$ for the same set of parameters. For this specific dc-side topology, $\rho_{\rm dc}$ is calculated by standard circuit variables available from the software ($\rho_{\rm dc} = \dot{v}_{\rm dc}/v_{\rm dc} = \imath_{\rm dc}/(C_{\rm dc}v_{\rm dc})$). Although the transient effect diminishes quickly for higher gain values, it can be seen that changing gain K_p^o has the expected effect on the quantity $\rho_{\rm dc}$. The modulation of both ρ_v , $\rho_{\rm dc}$ by parameter K_p^o justifies the use of the generalized quantity $\overline{\rho}$ in (44). The use of identical gains for the dq channels, without loss of stability, also justifies the complex notation of (44). For this specific application, the ratio of dc to ac voltage is: $v_{n,\rm dc}/v_n = 0.35$.

E. Effect of the Active Power Droop

Fig. 18 shows the effect of the active power droop parameter m_p on the internal frequency and on the frequency at the converter bus after the contingency. The effect of this outer-loop parameter on the frequency is more impactful than the previous cases that concern controllers with faster time scales. Particularly for the internal frequency, the time response is separated by a full order of magnitude compared to the frequency at the bus. Finally, note that although the effects are larger for this case, the selected unit for the figures (pu x10⁻³) is kept the same with the previous examples for consistency.

F. Effect of the Virtual Shaft

Fig. 19 shows the frequency response when the damping parameter D_p of the VSM control of (48) is changed. The contingency for this case is a disconnection of the load at bus 6 at t = 1 s. Similar to the previous cases, the variation of

the control parameters affects the internal frequency of the converter to a greater extent than the one at the bus. The examples discussed in the next section show how this property can be utilized to improve the control of the converters.

G. Control Applications

This section illustrates the use of the converter internal frequency on control applications by means of two examples, one for GFL and one for GFM. While the frequency at the bus of the converter can be estimated (e.g., through a PLL), it is unclear whether it is the best signal one can use. For both GFL and GFM cases, a Primary Frequency Regulator (PFR) is added on top of the outer control layers described in the previous sections. Specifically, the PFR is added on top of the power controller (equation (37) and Fig. 3) for the GFL case and on top of the active power droop controller (equation (47) and Fig. 7 (a)) for the GFM case. The PFR is composed of a low-pass filter, a washout filter and a hard-limit [41]. The PFR modifies the active power reference p_{ref} by quantity $\Delta p_{\rm ref}$ based on an input frequency signal. The signals that are used for the comparison are the imaginary part of the CF at the converter bus and the imaginary part of the internal converter frequency as give by equations (7), (33) and (47). The comparison of the dynamic performance of the primary controllers based on the bus frequency and on the internal ones is the main objective of this section.

Fig. 20 shows the PFR input frequency signals after the contingency for the GFL case. Different type of signals and different values of PLL proportional gain K_p^{PLL} are used. In the zoomed-in version, it can be seen how the faster PLL $(K_n^{\text{PLL}} = 1)$ matches the exact frequency after the initial transient. The slower PLLs introduce oscillations that remain for several seconds after the contingency. The impact of this control on the bus frequency is shown in Fig. 21. On the other hand, the faster the PLLs, the better the frequency response. This result is consistent with the case that uses the exact frequency signal. It can be concluded that the internal frequency of the converter can have similar results with the exact frequency at the bus when used for frequency control. This particularly useful for the cases where the exact signal is not available. However, careful tuning of the PLL parameters should be considered so that its bandwidth is suitably fast for the application while preventing undesired coupling with the dynamics of (weak) grids [42].

Fig. 22 shows the output signal of the PFR for the GFM case. The exact frequency at the bus is used as well as the internal frequency of the converter with different values of the active power droop gain m_p . It can be seen how the different input signals affects the controller operation until its output is saturated at t = 3 s. Fig. 23 shows the effect of the frequency control at the bus frequency. Results indicate that, if the droop gain is sufficiently large, the droop PFR outperforms the CF PFR for the first two seconds after the contingency.

H. Comparison between VSM and Synchronous Generator

This section illustrates how the CF can be used as a *metric* to directly compare the transient operation of a GFM converter



Fig. 20. Input signal (full and zoomed-in version) of the frequency control for the GFL case. Disconnection at t = 1 s of the load at bus 5 of the WSCC 9-bus system. The simulation is repeated for two types of input signal and different values of the PLL proportional gain $K_p^{\rm PLL}$.



Fig. 21. Imaginary part of the CF as seen at bus 5 of the WSCC 9-bus system during the disconnection at t = 1 s of the load at bus 5. Application of frequency control for the GFL case. The simulation is repeated without frequency control, for two types of input signal and for different values of the PLL proportional gain $K_p^{\rm PLL}$.

and a synchronous machine. For this case, the synchronous machine connected to bus 2 of the network, shown in Fig. 9, has been substituted with a VSM. The capacity of the VSM and the tuning of its control parameters are selected in such a way that the VSM operates similarly to the original synchronous machine. Finally, the same contingency considered in previous sections is applied also in this scenario.

Figures 24 and 25 show the real and imaginary parts, respectively, of the CF at bus 2, in the two scenarios, namely with synchronous machine and with VSM connected to the bus. An additional scenario is shown in these figures, i.e., a case for which the GFL converter at bus 5 is disconnected



Fig. 22. Output signal of the frequency control for the GFM case. Disconnection at t = 1 s of the load at bus 5 of the WSCC 9-bus system. The simulation is repeated without frequency control, for two types of input signal and for different values of the active power droop parameter m_p .



Fig. 23. Imaginary part of the CF as seen at bus 5 of the WSCC 9-bus system during the disconnection at t = 1 s of the load at bus 5. Application of frequency control for the GFM case. The simulation is repeated without frequency control, for two types of input signal and for different values of the active power droop parameter m_p .

and the VSM is the only converter in the system. It is shown that the control and implementation aspects of the VSM lead to different transient operation compared to a traditional synchronous generator. CF captures the discrepancies in the transient operation and can be used as a metric to directly compare them. Different trends for the system transient performance are identified through the use of the different quantities ρ_v , ω_v . Specifically, the magnitude of postcontingency oscillations of ρ_v is smaller for the synchronous machine while for ω_v , the use of the VSM results in smaller oscillations. The frequency response with the disconnected GFL converter is nearly identical with the case for which it is connected. This fact highlights that the interaction between the different converter control configurations does not affect CF significantly.



Fig. 24. Real part of the CF as seen at bus 2 of the WSCC 9-bus system during the disconnection at t = 1 s of the load at bus 5. The simulation is repeated with a synchronous generator connected to bus 2, a VSM substituting the synchronous generator and the GFL converter at bus 5 disconnected.



Fig. 25. Imaginary part of the CF as seen at bus 2 of the WSCC 9-bus system during the disconnection at t = 1 s of the load at bus 5. The simulation is repeated with a synchronous generator connected to bus 2, a VSM substituting the synchronous generator and the GFL converter at bus 5 disconnected.

VII. CONCLUSION

In this paper, the concept of CF is utilized to develop a taxonomy of different power-converter control schemes. Both GFL and GFM control configurations are studied and their effect on the local frequency is analytically derived. Theoretical results are complemented with a case study based on a modified model of the WSCC 9-bus system, where the derived analytical formulations are used for control applications for both GFLs and GFMs.

Results show that CF approach decouples the contribution on the local frequency of each sub-controller and identifies critical control parameters. For all converters, the current controller is shown to represent a constant translation of the real part of the CF while the synchronization control, regardless of its type, affects the imaginary part. For GFL configurations, the PLL parameters are shown to have the largest impact on the local frequency. For GFM, active power droop parameter as well as VSM damping parameter are shown to affect the frequency response after a contingency. For the GFL control application case, the internal frequency of the converter, used as an input to a PFR, achieves the same frequency response with the exact frequency measurement, provided that the PLL is sufficiently fast. For the GFM case, the internal frequency of the controller achieves a better transient response than the exact frequency.

In this work, the dynamic effect of conventional controllers on the frequency at their point of connection was presented and classified. Future work will focus on extending the use of the calculated internal frequencies of the converters for control applications. The potential of using non-conventional controllers based on CF or controllers based on non-conventional input signals, such as the real part and the magnitude of the CF, will also be further explored. Finally, the effect on CF of multiple converters, their dynamic interaction and the impact of this interaction on converter frequency control will also be studied.

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